

## WEST Search History

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DATE: Sunday, February 17, 2008

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
		<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>	
<input checked="" type="checkbox"/>	L29	L28 and l3	20
<input checked="" type="checkbox"/>	L28	((serializ\$3 near2 deserializ\$3) or serdes\$2) near7 (plural\$4 or multipl\$4 or several\$3 or numerous\$2)) and (transceiver\$ or (transmitter same receiver\$)) same (pll or (phase\$ near2 lock\$3))	33
<input checked="" type="checkbox"/>	L27	((serializ\$3 near2 deserializ\$3) or serdes\$2) near7 (plural\$4 or multipl\$4 or several\$3 or numerous\$2)) same (transceiver\$ or (transmitter same receiver\$)) same (pll or (phase\$ near2 lock\$3))	6
<input checked="" type="checkbox"/>	L26	("20030059036" "20040098634" "20050034009" "20050169417" "6950956" "7155006").PN.	10
<input checked="" type="checkbox"/>	L25	20040143734 and (pll or (phase\$ near2 lock\$3))	1
<input checked="" type="checkbox"/>	L24	20040143734	2
<input checked="" type="checkbox"/>	L23	L22 and L21 and L3	167
<input checked="" type="checkbox"/>	L22	((serializ\$3 near2 deserializ\$3) or serdes\$2) near7 (plural\$4 or multipl\$4 or several\$3 or numerous\$2)	503
<input checked="" type="checkbox"/>	L21	709/\$5.ccls. or 710/\$5.ccls. or 713/\$5.ccls. or 370/\$5.ccls. or 375/\$5.ccls.	293820
<input checked="" type="checkbox"/>	L20	((serializ\$3 near2 deserializ\$3) or serdes\$2) near6 core\$) near7 (plural\$4 or multipl\$4)	34
<input checked="" type="checkbox"/>	L19	((serializ\$3 near2 deserializ\$3) or serdes\$2) near6 core\$) and (disc near3 drive) and L3	7
<input checked="" type="checkbox"/>	L18	((serializ\$3 near2 deserializ\$3) or serdes) near6 core\$) and (substrate\$ and board) and (disc near3 drive) and L3	1
<input checked="" type="checkbox"/>	L17	((serializ\$3 near2 deserializ\$3) or serdes) near6 core\$) and (substrate\$ and board)	66
<input checked="" type="checkbox"/>	L16	((serializ\$3 near2 deserializ\$3) or serdes) near6 core\$) and (lane and substrate\$ and board)	3
<input checked="" type="checkbox"/>	L15	((serializ\$3 near2 deserializ\$3) or serdes) near6 core\$) same (lane and substrate\$ and board)	2
<input checked="" type="checkbox"/>	L14	20040120418	2
<input checked="" type="checkbox"/>	L13	L12 and L10 and L3	0
<input checked="" type="checkbox"/>	L12	((synchroniz\$3 or clock\$ or lock\$3) near6 ((serializ\$3 near2 deserializ\$3) or serdes))	673
<input checked="" type="checkbox"/>	L11	((synchroniz\$3 or clock\$ or lock\$3) near6 ((serializ\$3 near2 deserializ\$3) or serdes)) and (disc\$ near3 drive)	9
<input checked="" type="checkbox"/>	L10	(375/211).ccls. or (455/7).ccls. or (455/14).ccls. or (455/15).ccls. or (455/16).ccls. or (370/279).ccls. or (370/293).ccls. or (379/338).ccls.	2957

<input type="checkbox"/>	L9	20040179638	2
<input type="checkbox"/>	L8	L7 and L3	34
<input type="checkbox"/>	L7	((synchroniz\$3 or clock\$ or lock\$3) near6 ((serializ\$3 near2 deserializ\$3) or serdes)) same (transceiver\$))	65
<input type="checkbox"/>	L6	((synchroniz\$3 or clock\$ or lock\$3) near6 ((serializ\$3 near2 deserializ\$3) or serdes)) same (transceiver\$)) and (disc near3 drive)	7
<input type="checkbox"/>	L5	((synchroniz\$3 or clock\$ or lock\$3) near6 ((serializ\$3 near2 deserializ\$3) or serdes)) same (transceiver\$) same (disc near3 drive)	2
<input type="checkbox"/>	L4	L3 and L2	57
<input type="checkbox"/>	L3	@ad<=20040130	27853202
<input type="checkbox"/>	L2	((serializ\$3 near2 deserializ\$3) or serdes) same (disc near3 drive)	88
<input type="checkbox"/>	L1	((serializ\$3 near2 deserializ\$3) or serdes) same (phase near3 lack\$3) same (disc near3 drive)	0

END OF SEARCH HISTORY